

FEATRLRES

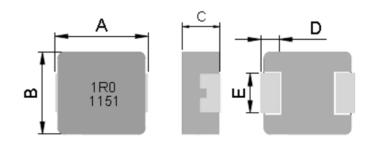
- Carbonyl Powder.
- Compact design.
- High current , low DCR , high efficiency.
- Very low acoustic noise and very low leakage flux noise.
- High reliability.
- 100% Lead(Pb)-Free and RoHS compliant.

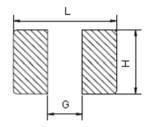
APPLICATIONS

- Note PC power system , incl. IMVP-6
- DC/DC converter .

CONFIGRLRATIONS & DIMENSIONS (unit in mm)

Recommended Land pattern





Туре	A	В	С	D	E	
HMPL0624H	7.0±0.3	6.6±0.3	2.2±0.2	1.8±0.3	3.0±0.3	

L	G	Н
7.7	2.5	3.5

Note:

- 1. The above PCB layout reference only.
- 2. Recommend solder paste thickness at 0.12mm and above.



ELECTRICAL CHARACTERISTICS

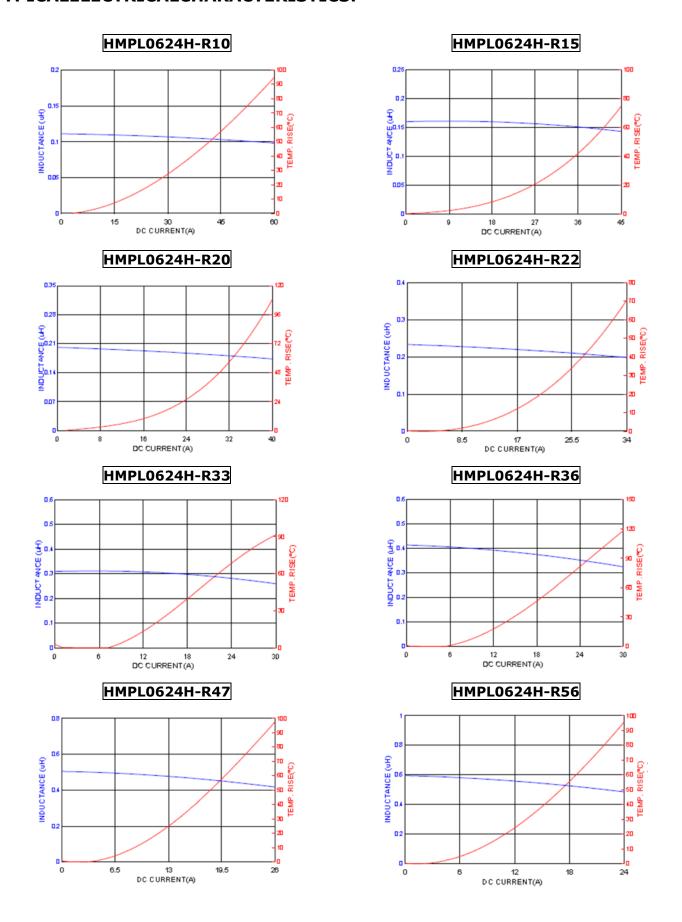
Part Number	Inductance L0		<u> </u>	DCR(mΩ)	DCR(mΩ)
	I rms (A) Typ. (uH)±20% @ 0 A	I sat (A) Typ.	Typ.@25 ℃	Max.@25℃	
HMPL0624H-R10YG-Z02-D	0.10±30%	30	70	1.4	1.7
HMPL0624H-R15YG-Z02-D	$0.15 \pm 30\%$	30	45	1.8	2.3
HMPL0624H-R20MG- Z02-D	$0.20 \pm 20\%$	23	40	1.9	2.8
HMPL0624H-R22MG- Z02-D	$0.22 \pm 20\%$	21	34	2.0	3.2
HMPL0624H-R33MG- Z02-D	$0.33 \pm 20\%$	18	30	3.6	4.4
HMPL0624H-R36MG- Z02-D	$0.36 \pm 20\%$	17	29	3.8	4.6
HMPL0624H-R47MG- Z02-D	$0.47 \pm 20\%$	15	26	4.8	5.1
HMPL0624H-R56MG- Z02-D	$0.56 \pm 20\%$	13	24	5.5	6.5
HMPL0624H-R60MG-Z02-D	$0.60 \pm 20\%$	13	22	5.7	6.9
HMPL0624H-R68MG- Z02-D	0.68±20%	13	21	6.4	7.2
HMPL0624H-R82MG- Z02-D	$0.82 \pm 20\%$	11	17	8.0	9.5
HMPL0624H-1R0MG- Z02-D	1.00±20%	11	16	10.5	13.5
HMPL0624H-1R5MG- Z02-D	1.50±20%	9	15	17	20
HMPL0624H-2R2MG- Z02-D	2.20±20%	7	14	23	28
HMPL0624H-3R3MG- Z02-D	$3.30 \pm 20\%$	6	10	34	39
HMPL0624H-4R7MG- Z02-D	4.70±20%	5.5	9	41	50
HMPL0624H-5R6MG- Z02-D	5.60±20%	5	8	56	62
HMPL0624H-6R8MG- Z02-D	6.80±20%	4	7	65	72
HMPL0624H-8R2MG- Z02-D	8.20±20%	3.6	6.0	81	95
HMPL0624H-100MG- Z02-D	10.0±20%	3.2	5.0	92	101
HMPL0624H-220MG- Z02-D	22.0±20%	1.8	3.0	185	215

Note:

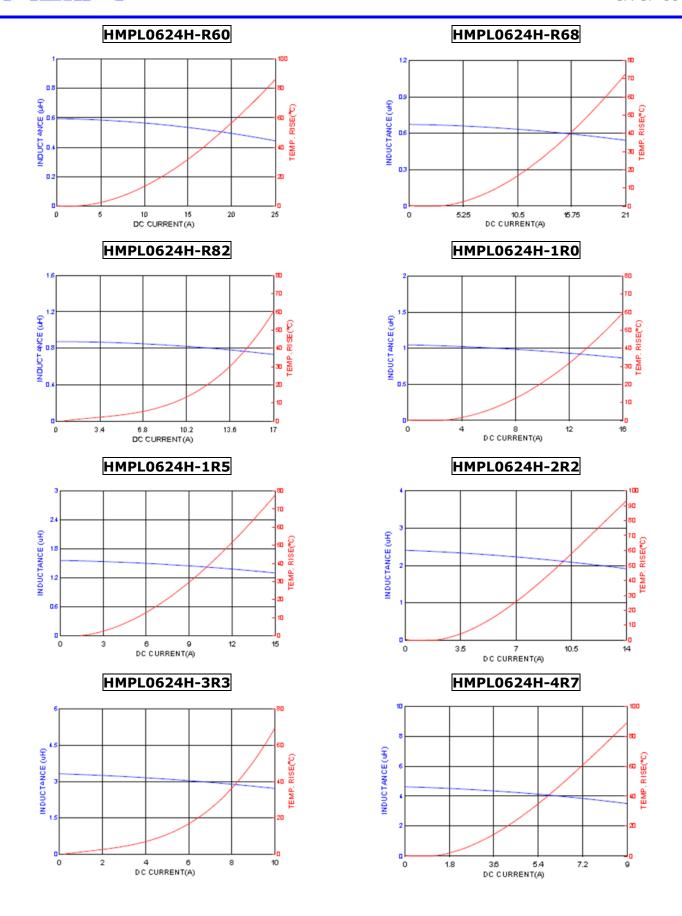
- 1.Test frequency : Ls : 100KHz /1.0V. 2.All test data referenced to 25°C ambient.
- 3.Testing Instrument(or equ): L: HP4284A,CH11025,CH3302,CH1320,CH1320S LCR METER / Rdc:CH16502,Agilent33420A MICRO OHMMETER.
- 4.Heat Rated Current (Irms) will cause the coil temperature rise approximately $\,\Delta T$ of 40°C
- 5. Saturation Current (Isat) will cause L0 to drop approximately 20%.
- 6.The part temperature (ambient + temp rise) should not exceed 125° Cunder worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Special inquiries besides the above common used types can be met on your requirement.



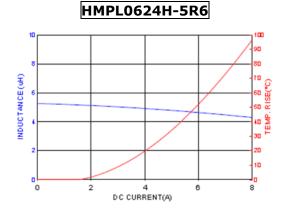
TYPICALELECTRICALCHARACTERISTICS:





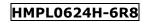


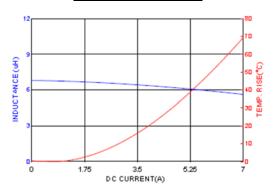




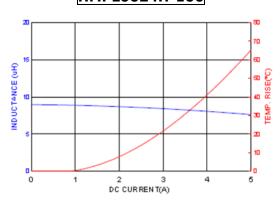








HMPL0624H-100





Reliability and Test Condition

Item	Performance	Test Condition				
Operating temperature	-40~+125℃ (Including self - temperature rise)					
110~+40°C,50~60%RH (Product with taping) 240~+125°C (on board)						
Electrical Performance Test	·					
Inductance	Prince the standard of the sta	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.				
DCR	Refer to standard electrical characteristics list.	CH16502,Agilent33420A Micro-Ohm Meter.				
Saturation Current (Isat)	Approximately∆L30%	Saturation DC Current (Isat) will cause L0 to drop △L(%)				
Heat Rated Current (Irms)	Approximately △T40°C	Heat Rated Current (Irms) will cause the coil temperature rise $\triangle T(^{\circ}C)$. 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer				
Reliability Test						
Life Test		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles) Temperature: 125±2°C (Inductor) Applied current: rated current Duration: 1000±12hrs Measured at room temperature after placing for 24±2 hrs				
Load Humidity		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Humidity: 85±2 * R.H, Temperature: 85°C±2°C Duration: 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs				
Moisture Resistance	Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles 1. Baked at50° $\mathbb C$ for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to $65\pm2°\mathbb C$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25°\mathbb C$ in 2.5hrs. 3. Raise temperature to $65\pm2°\mathbb C$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25°\mathbb C$ in 2.5hrs, keep at $25°\mathbb C$ for 2 hrs then keep at $-10°\mathbb C$ for 3 hrs 4. Keep at $25°\mathbb C$ 80-100%RH for 15min and vibrate at the frequency of 10 to $55°\mathbb C$ 80-100%RH for 15min and vibrate at the frequency of 10 to $55°\mathbb C$ 80-100%RH for 15min and vibrate at the frequency of 10 to $55°\mathbb C$ 80-100%RH for 15min and vibrate at the frequency of 10 to $55°\mathbb C$ 80-100%RH for 15min and vibrate at the frequency of 10 to $55°\mathbb C$ 80-100%RH for 15min and vibrate at the frequency of 10 to $55°\mathbb C$ 80-100%RH for 15min and vibrate at the frequency of 10 to $55°\mathbb C$ 80-100%RH for 15min and vibrate at the frequency of 10 to $55°\mathbb C$ 80-100%RH for 15min and vibrate at the frequency of 10 to $55°\mathbb C$ 80-100%RH for 15min and vibrate at the frequency of 10 to $55°\mathbb C$ 80-100%RH for 15min and vibrate at the frequency of 10 to $55°\mathbb C$ 80-100%RH for 15min and vibrate at the frequency of 10 to $55°\mathbb C$ 80-100%RH for $5°\mathbb C$ 80-1000%RH for $5°\mathbb C$ 80-10000%RH for $5°\mathbb C$ 80-10000%RH for $5°\mathbb C$ 80-10000%RH for $5°\mathbb C$ 80-10				
Thermal shock Vibration		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Condition for 1 cycle Step1: -40±2°C 30±5min Step2: 25±2°C ≤0.5min Step3: 125±2°C 30±5min Number of cycles: 500 Measured at room temperature after placing for 24±2 hrs Oscillation Frequency: 10 ~ 2K ~ 10Hz for 20 minutes Equipment: Vibration checker				
Bending		Total Amplitude:1.52mm±10% Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations). Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm				
Shock	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	duration of 10 sec. Type Peak value (g's) Normal duration (D) (ms) Wave form (Vi)ft/sec Velocity change (Vi)ft/sec SMD 50 11 Half-sine 11.3 Lead 50 11 Half-sine 11.3				
Solder ability	More than 95% of the terminal electrode should be covered with solder.	Preheat: 150℃,60sec.₅ Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5℃ ∘				



		Flux for lead free: Rosin. 9.5% ∘
		Dip time: 4±1sec ∘
		Depth: completely cover the termination
		Depth: completely cover the termination
Resistance to Soldering Heat		Temperature(°C) Time(s) Temperature ramp/immersion and emersion rate Number of heat cycles
		260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s 1
Terminal Strength	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value e	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020DClassification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a force(>0805:1kg , <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.

Note: When there are questions concerning measurement result: measurement shall be made after 48 ± 2 hours of recovery under the standard condition.